

L Number	Hits	Search Text	DB	Time stamp
-	1166	smp with symmetric with processor	USPAT; US-PGPUB; EPO; JPO	2004/09/17 16:15
-	0	(smp with symmetric with processor) and (backward with compatibility)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:28
-	55	(smp with symmetric with processor) and (backward)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:17
-	60	(smp with symmetric with processor) and (smp with (different))	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:29
-	32	(smp with symmetric with processor) and (smp with (different) with processor)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:34
-	2	(smp with symmetric with processor) and (smp with heterogeneous with processor)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:41
-	2	6513057.URPN.	USPAT	2004/03/12 17:39
-	20	("3676860"   "3997895"   "4713806"   "4716526"   "5228127"   "5274831"   "5325526"   "5339443"   "5365606"   "5379428"   "5430850"   "5440752"   "5457780"   "5504670"   "5666519"   "5734910"   "5802373"   "5805895"   "5835775"   "5958061").PN.	USPAT	2004/03/12 17:40
-	126	(smp with symmetric with processor) and (smp with add\$7)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:41
-	87	(smp with symmetric with processor) and (smp with add\$7 with processor)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:42
-	87	(smp with symmetric with processor) and (smp with add\$7 with processor)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:43
-	64	(smp with symmetric with processor) and (smp with addition\$3 with processor)	USPAT; US-PGPUB; EPO; JPO	2004/03/12 17:43
-	2	6513057.URPN.	USPAT	2004/03/14 17:51
-	1	multprocess\$5 with symmetric	USPAT	2004/03/15 13:59
-	0	(multprocess\$5 with symmetric) and (backward with compatibl\$5)	USPAT	2004/03/15 13:59
-	0	(multprocess\$5 with symmetric) and (backward with compatibl\$5)	USPAT	2004/03/15 13:59
-	1	multprocess\$5 with symmetric	USPAT	2004/03/15 13:59
-	0	(multprocess\$5 with symmetric) and (backward)	USPAT	2004/03/15 13:59
-	0	(multprocess\$5) and (backward with compatibl\$5)	USPAT	2004/03/15 14:00
-	1019	backward with compatibl\$5	USPAT	2004/03/15 14:00
-	40	backward with compatibl\$5 with processor	USPAT	2004/03/15 14:00
-	1147	(OS (operating near3 system)) same (cache with coheren\$4)	USPAT; US-PGPUB; EPO; JPO	2004/09/17 16:16
-	0	(OS (operating near3 system)) same (cache with coheren\$4) same (work with allocat\$5)	USPAT; US-PGPUB; EPO; JPO	2004/09/17 16:17
-	9	(OS (operating near3 system)) same (cache with coheren\$4) and (work with allocat\$5)	USPAT; US-PGPUB; EPO; JPO	2004/09/17 17:22
-	12	(OS (operating near3 system)) same (cache with coheren\$4) same (topology)	USPAT; US-PGPUB; EPO; JPO	2004/09/17 17:23

-	17	(cache with coheren\$4) same (cache with topology)	USPAT; US-PGPUB; EPO; JPO	2004/09/19 19:59
-	5	(cache with coheren\$4) same (efficient with allocat\$5)	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:02
-	94	(cache with coheren\$4) and (efficient with allocat\$5)	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:02
-	49	(cache with coheren\$4) and (cache same (efficient with allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:03
-	1	(cache with coheren\$4) and ((cache with topology) same (efficient with allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:08
-	1	(cache with coheren\$4) and ((cache with topology) same (efficient near4 allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:08
-	28	(cache with coheren\$4) and ((cache) same (efficient near4 allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:08
-	0	(cache with coheren\$4) and ((cache) same (work near4 efficient near4 allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:09
-	0	(cache with coheren\$4) and ((cache) same (instruction near4 efficient near4 allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:09
-	0	(cache with coheren\$4) and ((cache) same (operation near4 efficient near4 allocat\$5))	USPAT; US-PGPUB; EPO; JPO	2004/09/19 20:09
-	323	712/32.cc1s.	USPAT	2004/09/19 20:23